

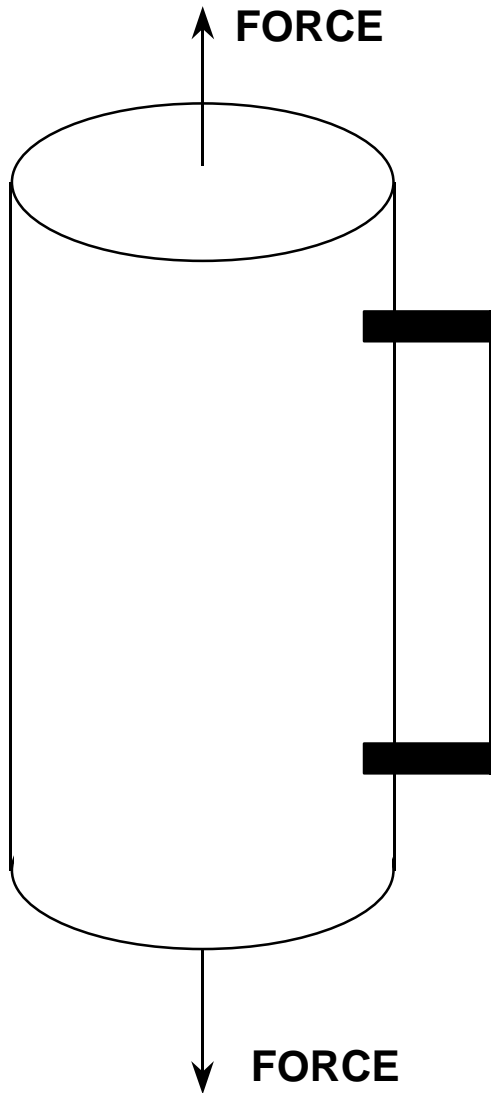
PRACTICAL DESIGN TECHNIQUES FOR SENSOR SIGNAL CONDITIONING

- 1 Introduction**
- 2 Bridge Circuits**
- 3 Amplifiers for Signal Conditioning**
- 4 Strain, Force, Pressure, and Flow Measurements**
- 5 High Impedance Sensors**
- 6 Position and Motion Sensors**
- 7 Temperature Sensors**
- 8 ADCs for Signal Conditioning**
- 9 Smart Sensors**
- 10 Hardware Design Techniques**

STRAIN GAGE BASED MEASUREMENTS

- **Strain:** **Strain Gage, PiezoElectric Transducers**
- **Force:** **Load Cell**
- **Pressure:** **Diaphragm to Force to Strain Gage**
- **Flow:** **Differential Pressure Techniques**

UNBONDED WIRE STRAIN GAGE



STRAIN
SENSING
WIRE

AREA = A
LENGTH = L
RESISTIVITY = ρ
RESISTANCE = R

$$R = \frac{\rho L}{A}$$

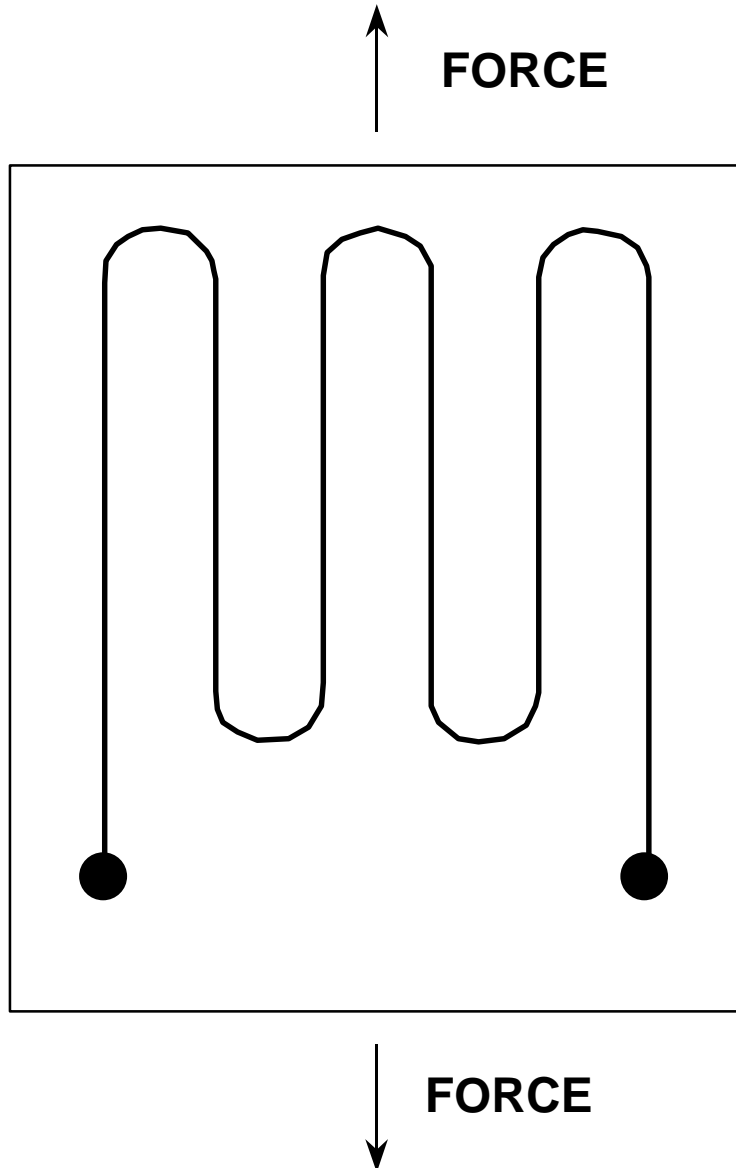
$$\frac{\Delta R}{R} = GF \cdot \frac{\Delta L}{L}$$

GF = GAGE FACTOR
2 TO 4.5 FOR METALS
>150 FOR SEMICONDUCTORS

$$\frac{\Delta L}{L} = \text{MICROSTRAINS } (\mu\epsilon)$$

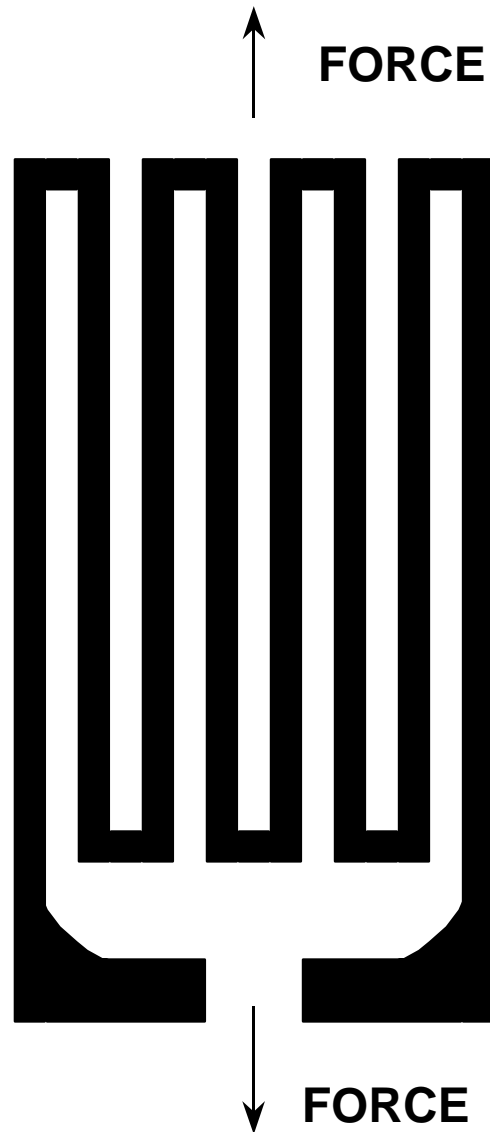
$$1 \mu\epsilon = 1 \times 10^{-6} \text{ cm / cm} = 1 \text{ ppm}$$

BONDED WIRE STRAIN GAGE



- SMALL SURFACE AREA
- LOW LEAKAGE
- HIGH ISOLATION

METAL FOIL STRAIN GAGE

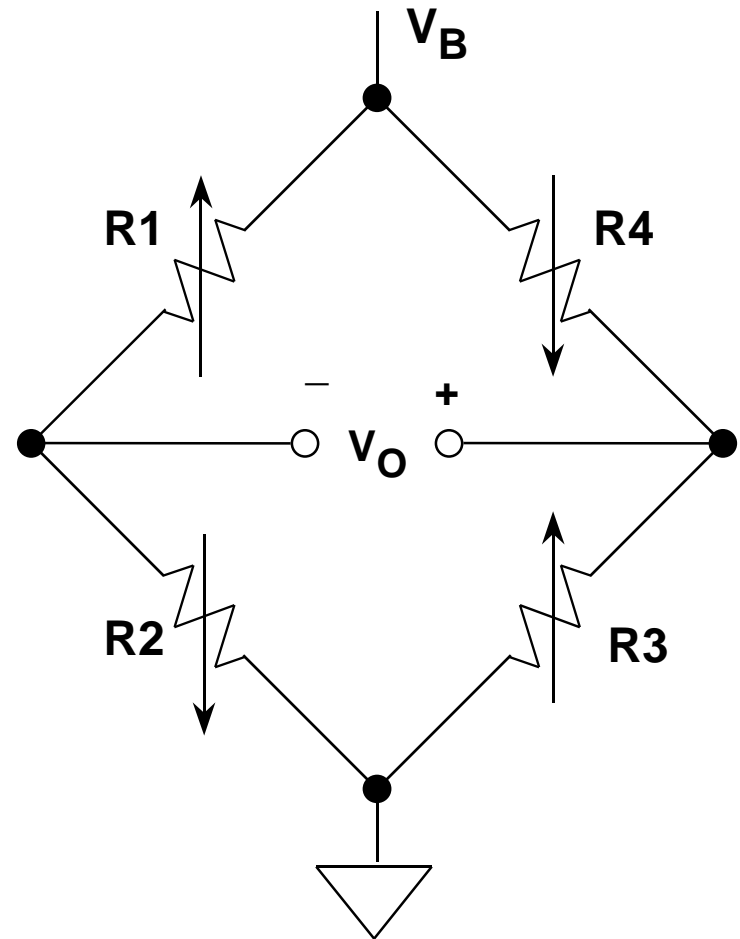
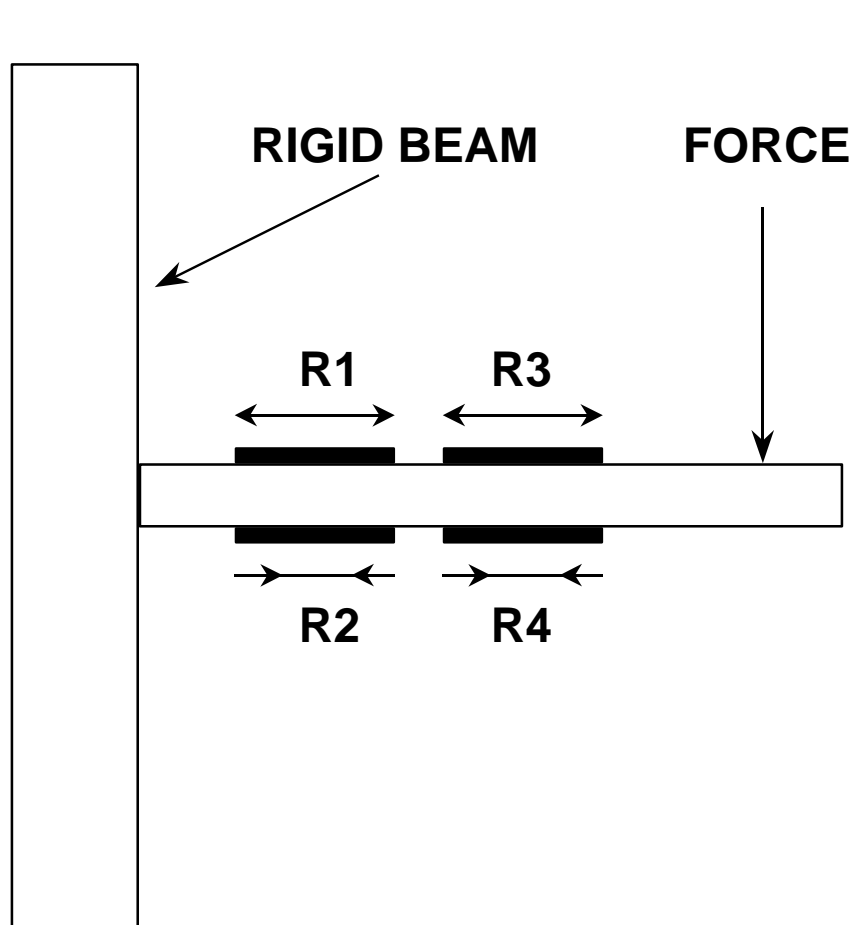


- PHOTO ETCHING TECHNIQUE
- LARGE AREA
- STABLE OVER TEMPERATURE
- THIN CROSS SECTION
- GOOD HEAT DISSIPATION

COMPARISON BETWEEN METAL AND SEMICONDUCTOR STRAIN GAGES

PARAMETER	METAL STRAIN GAGE	SEMICONDUCTOR STRAIN GAGE
Measurement Range	0.1 to 40,000 $\mu\epsilon$	0.001 to 3000 $\mu\epsilon$
Gage Factor	2.0 to 4.5	50 to 200
Resistance, Ω	120, 350, 600, ..., 5000	1000 to 5000
Resistance Tolerance	0.1% to 0.2%	1% to 2%
Size, mm	0.4 to 150 Standard: 3 to 6	1 to 5

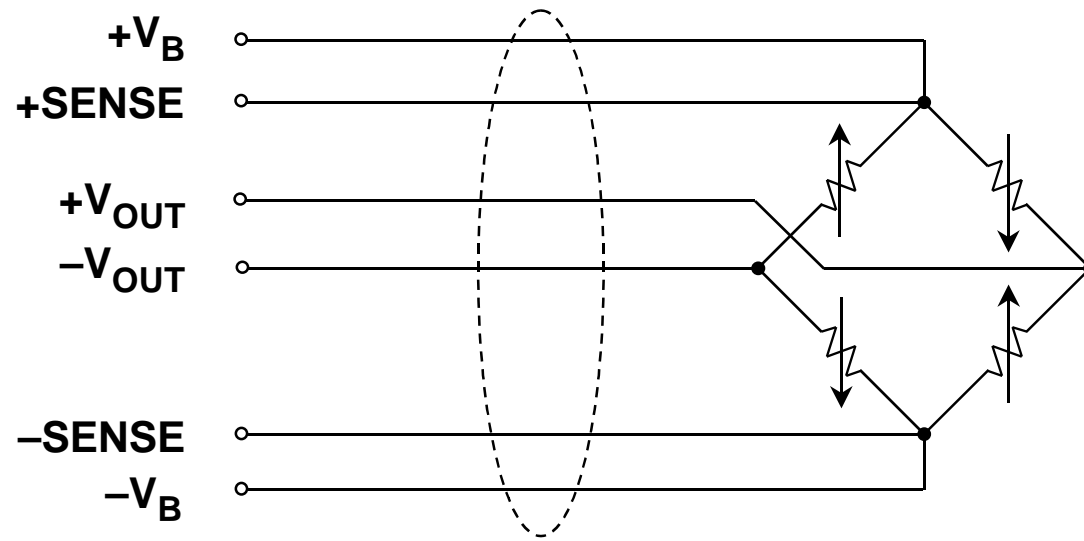
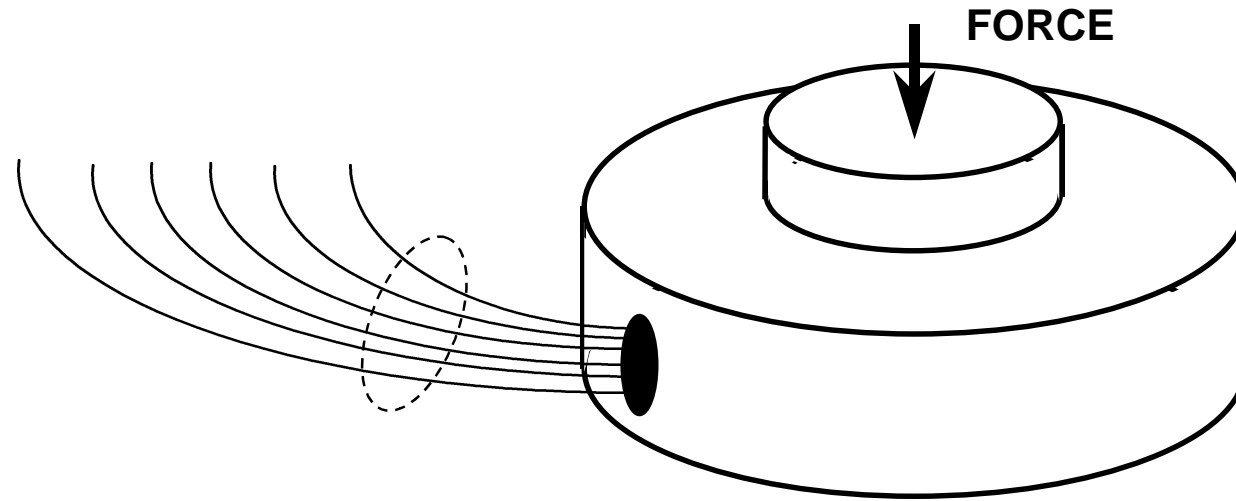
STRAIN GAGE BEAM FORCE SENSOR



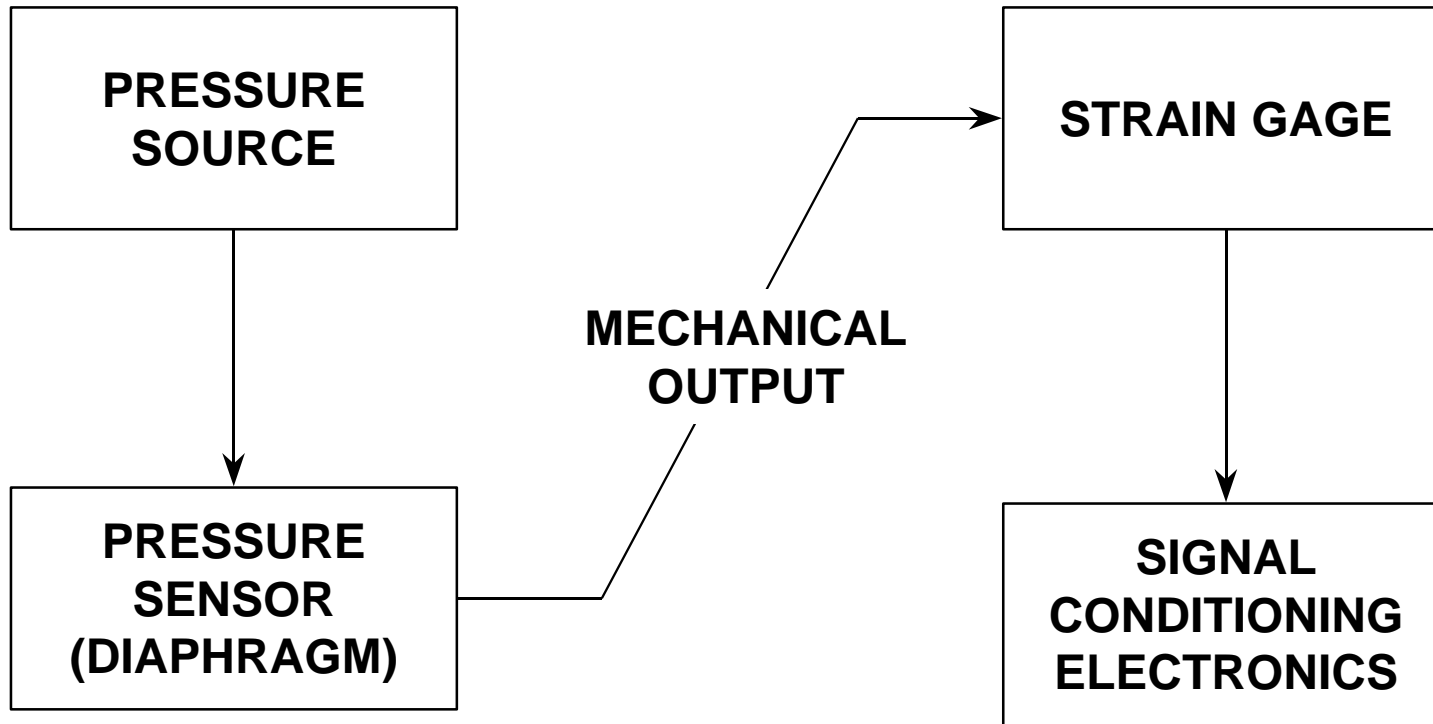
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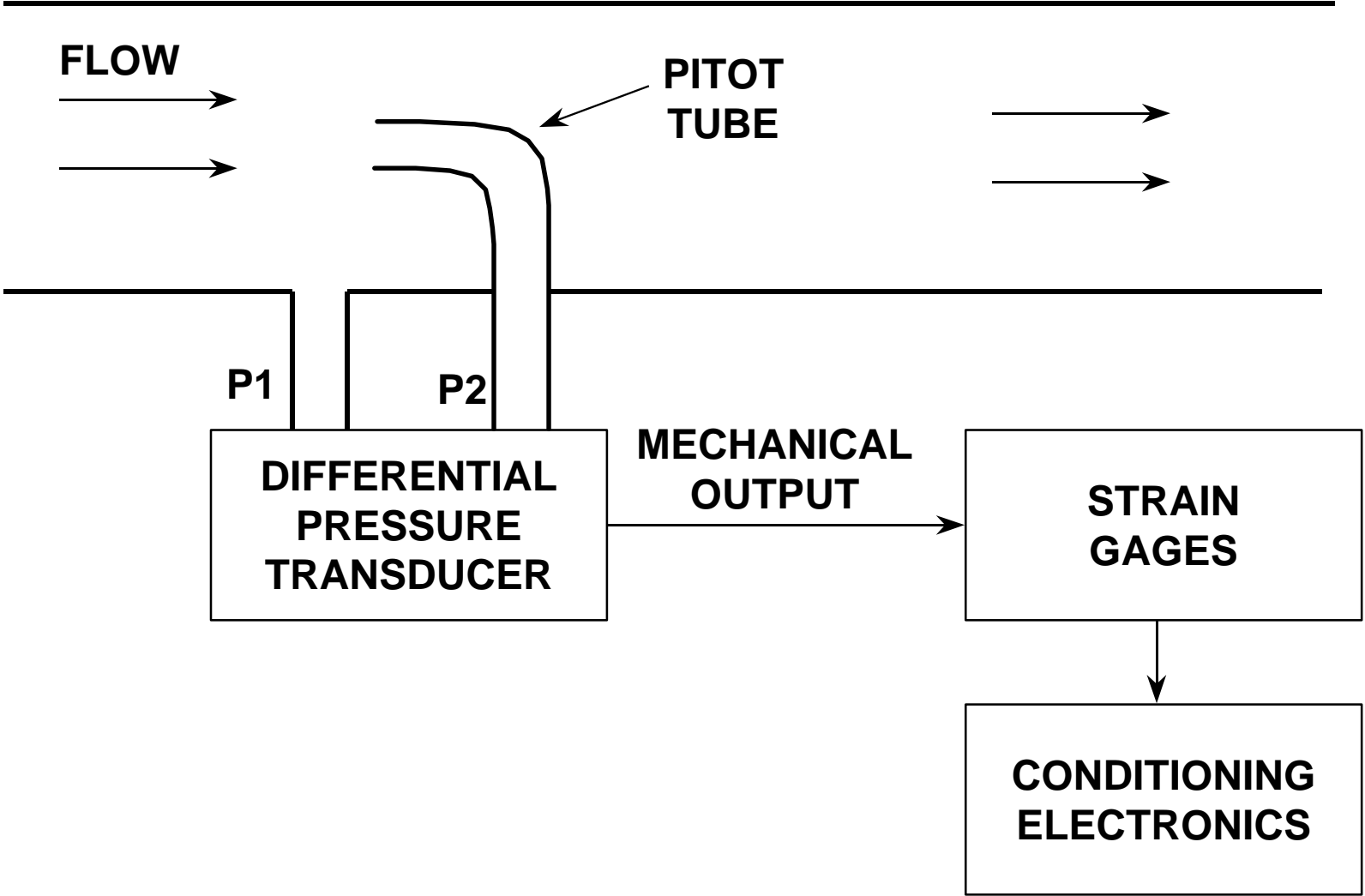
6-LEAD LOAD CELL



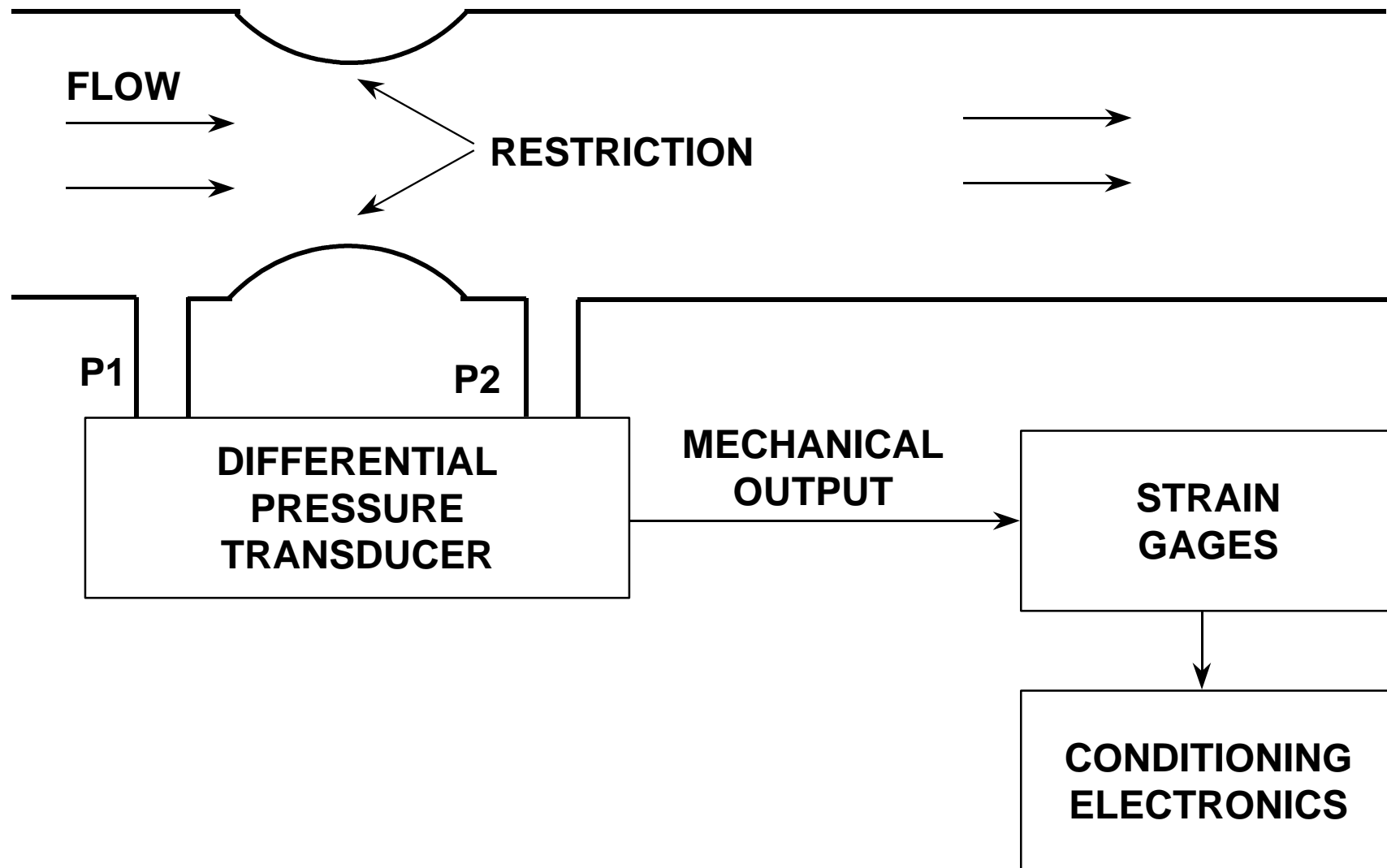
PRESSURE SENSORS



PITOT TUBE USED TO MEASURE FLOW RATE

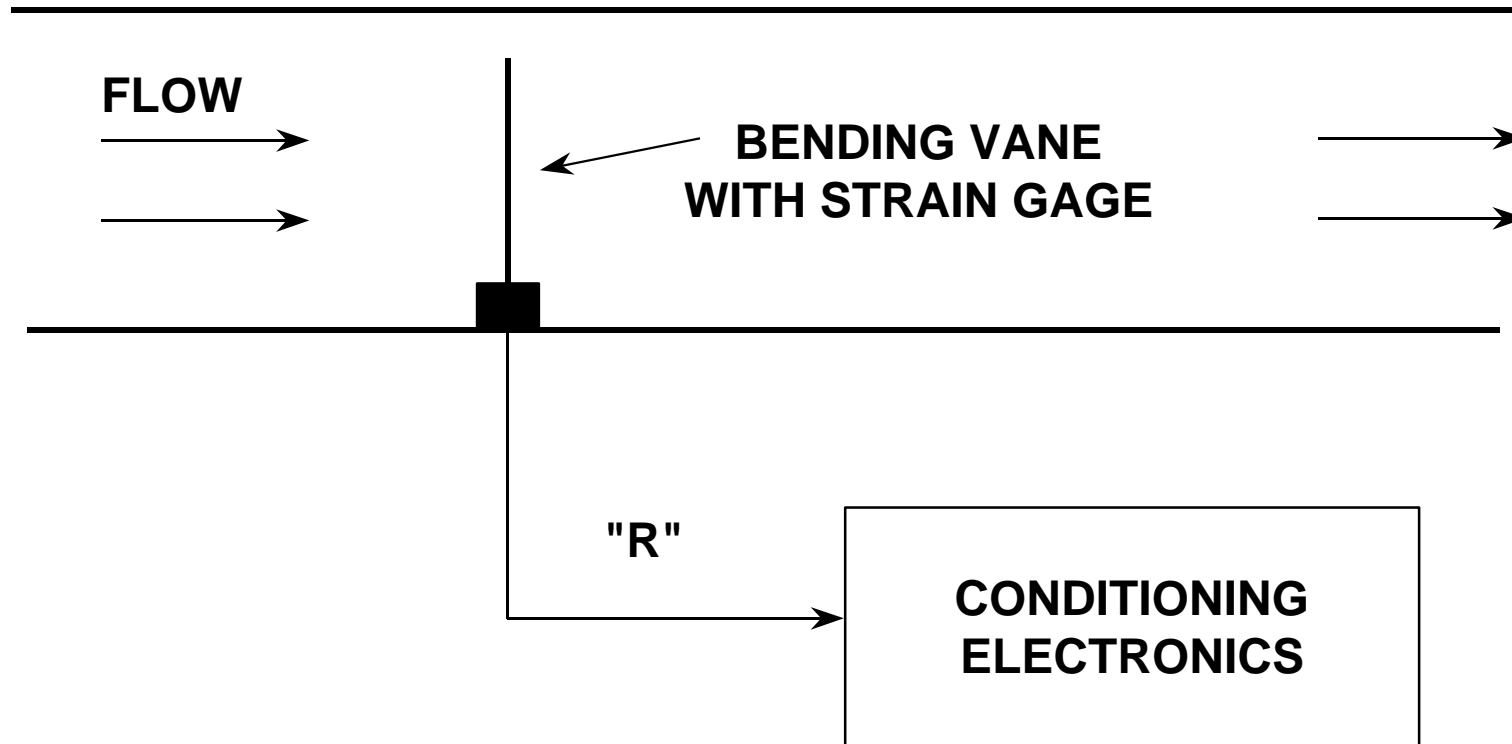


MEASURING FLOW RATE USING THE VENTURI EFFECT

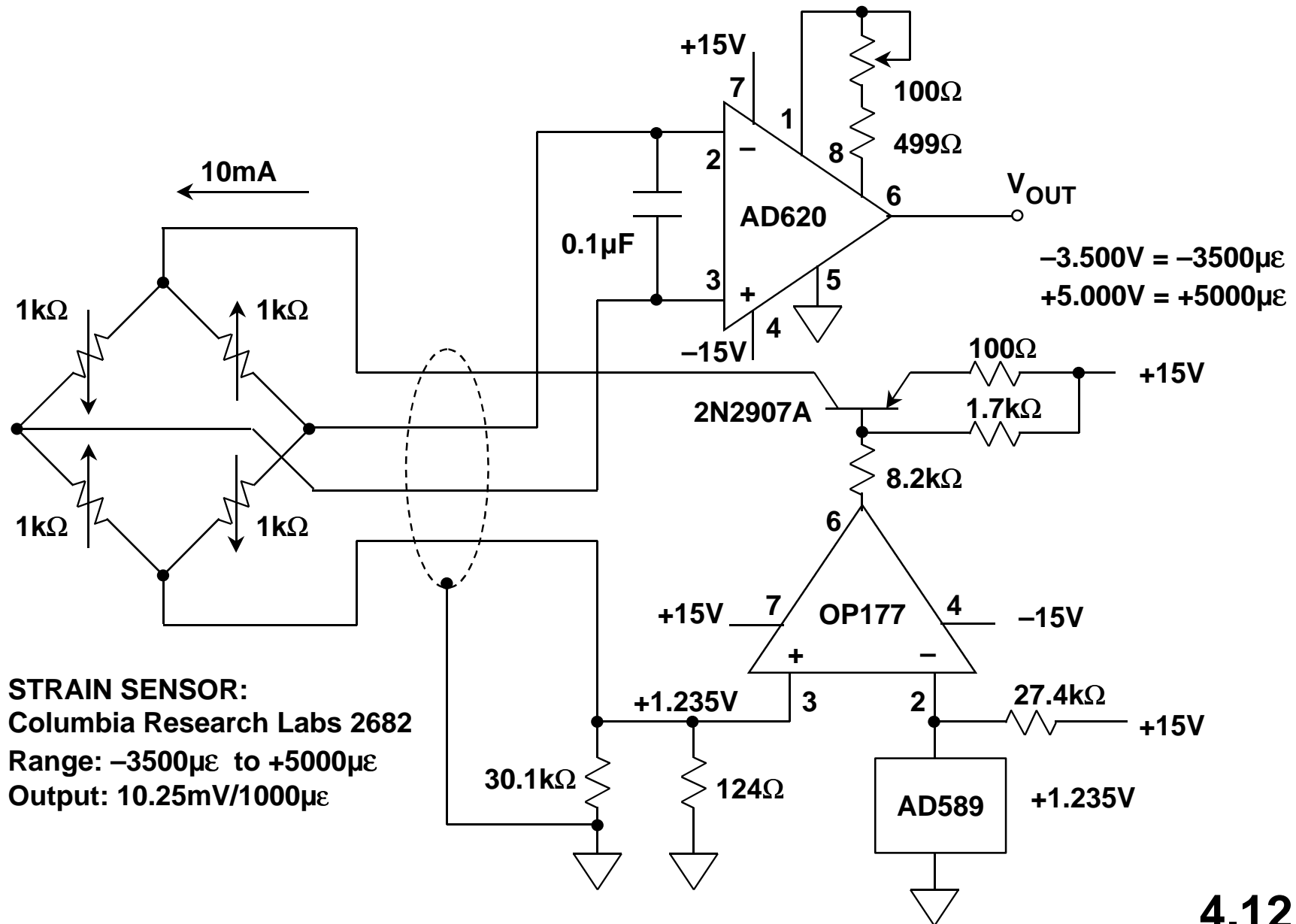


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BENDING VANE WITH STRAIN GAGE USED TO MEASURE FLOW RATE



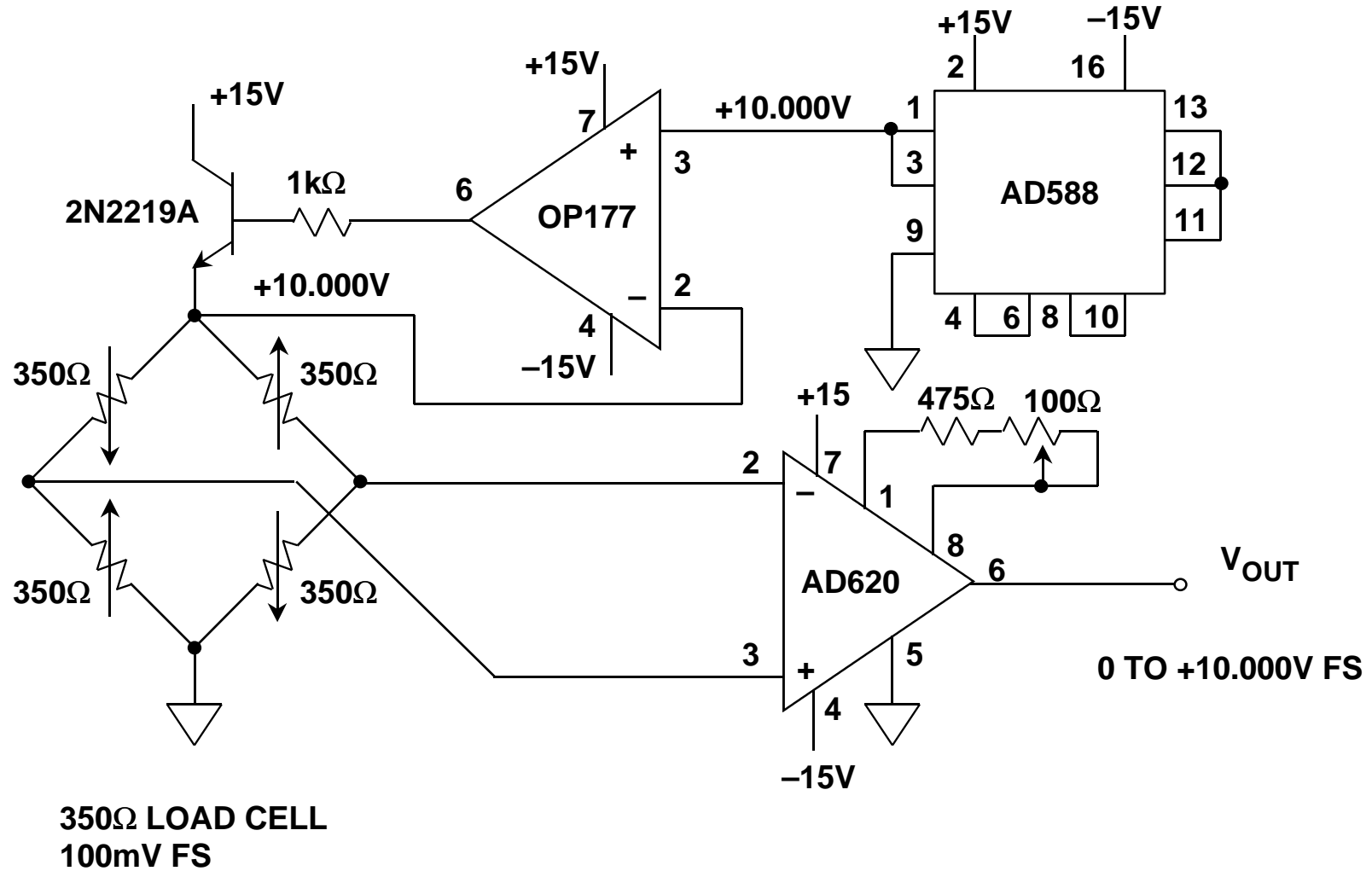
PRECISION STRAIN GAGE SENSOR AMPLIFIER



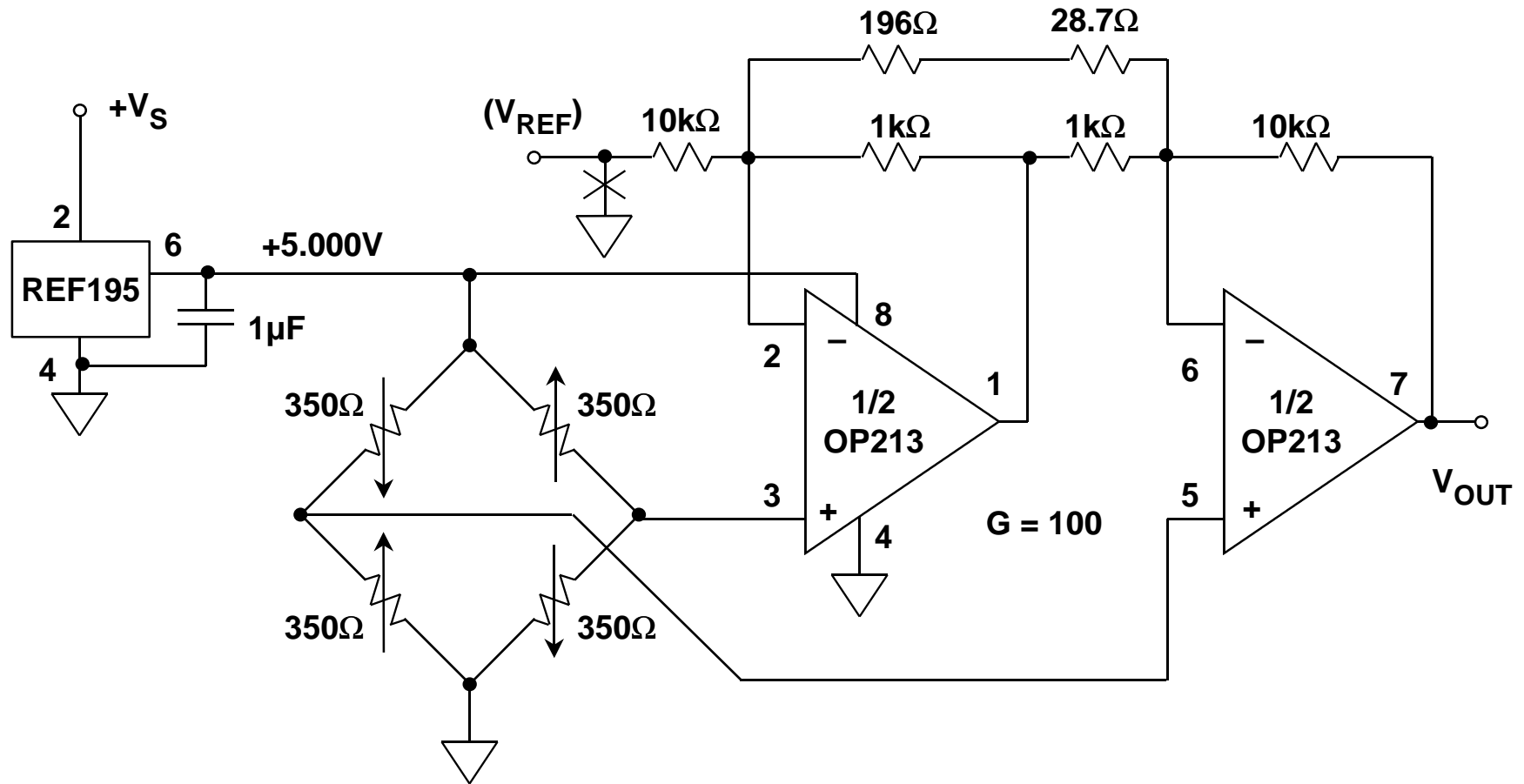
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PRECISION LOAD CELL AMPLIFIER



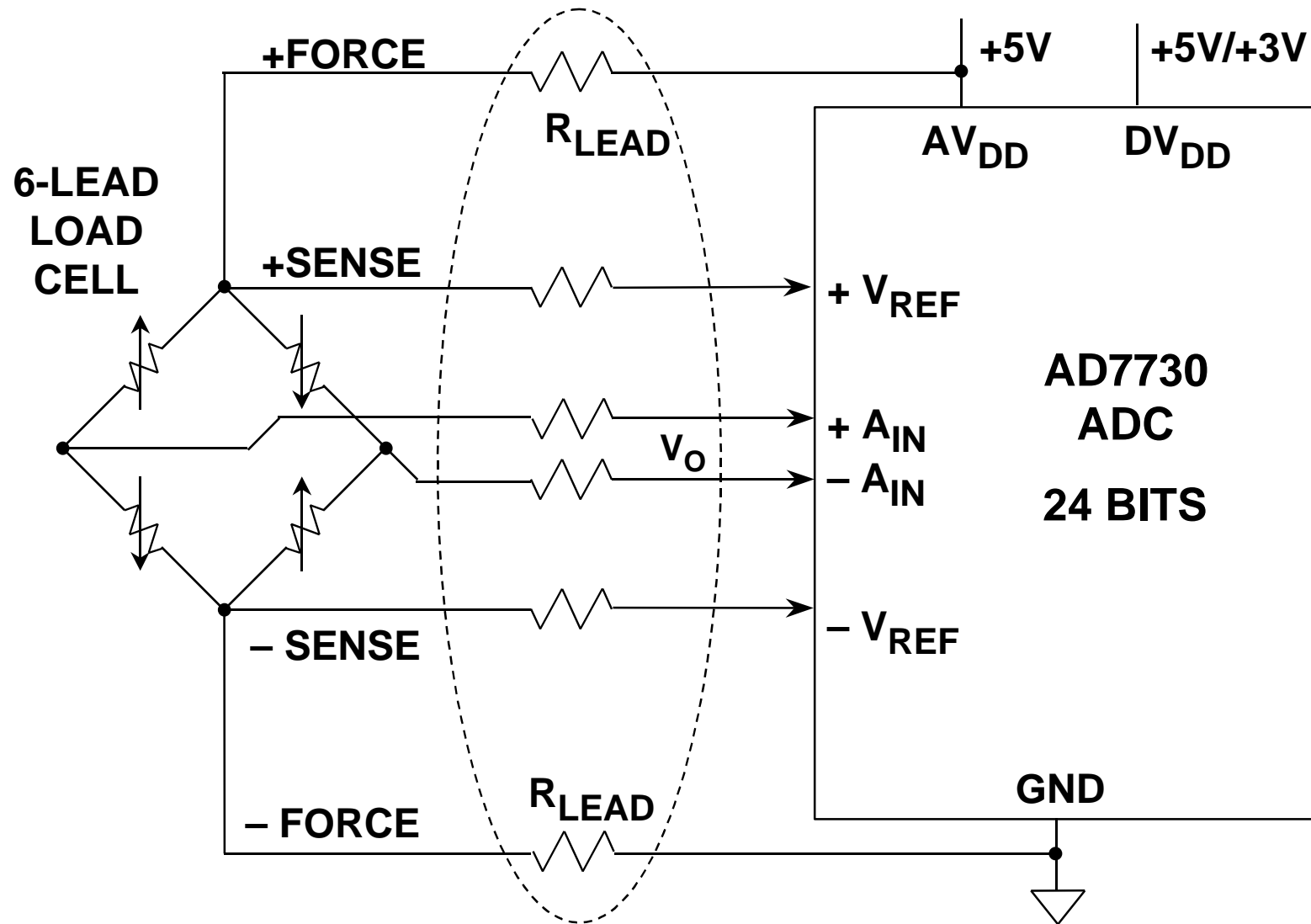
SINGLE SUPPLY LOAD CELL AMPLIFIER



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4.14

LOAD CELL APPLICATION USING THE AD7730 ADC



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4.15

PERFORMANCE OF AD7730 LOAD CELL ADC

■ Assume:

Fullscale Bridge Output of $\pm 10\text{mV}$, +5V Excitation

"Chop Mode" Activated

System Calibration Performed: Zero and Fullscale

■ Performance:

Noise RTI: 40nV rms, 264nV p-p

Noise-Free Resolution: $\approx 80,000$ Counts (16.5 bits)

Gain Nonlinearity: 18ppm

Gain Accuracy: $< 1\mu\text{V}$

Offset Voltage: $< 1\mu\text{V}$

Offset Drift: $0.5 \mu\text{V}/^\circ\text{C}$

Gain Drift: 2ppm/ $^\circ\text{C}$

Note: Gain and Offset Drift Removable with System Recalibration